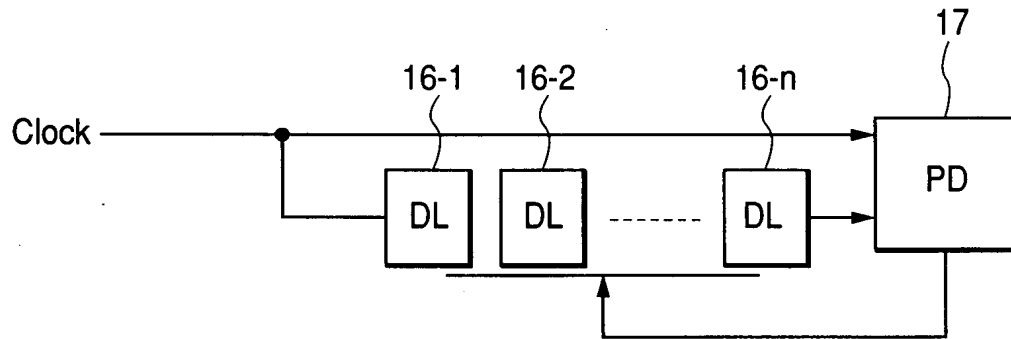




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FIG. 3
PRIOR ART



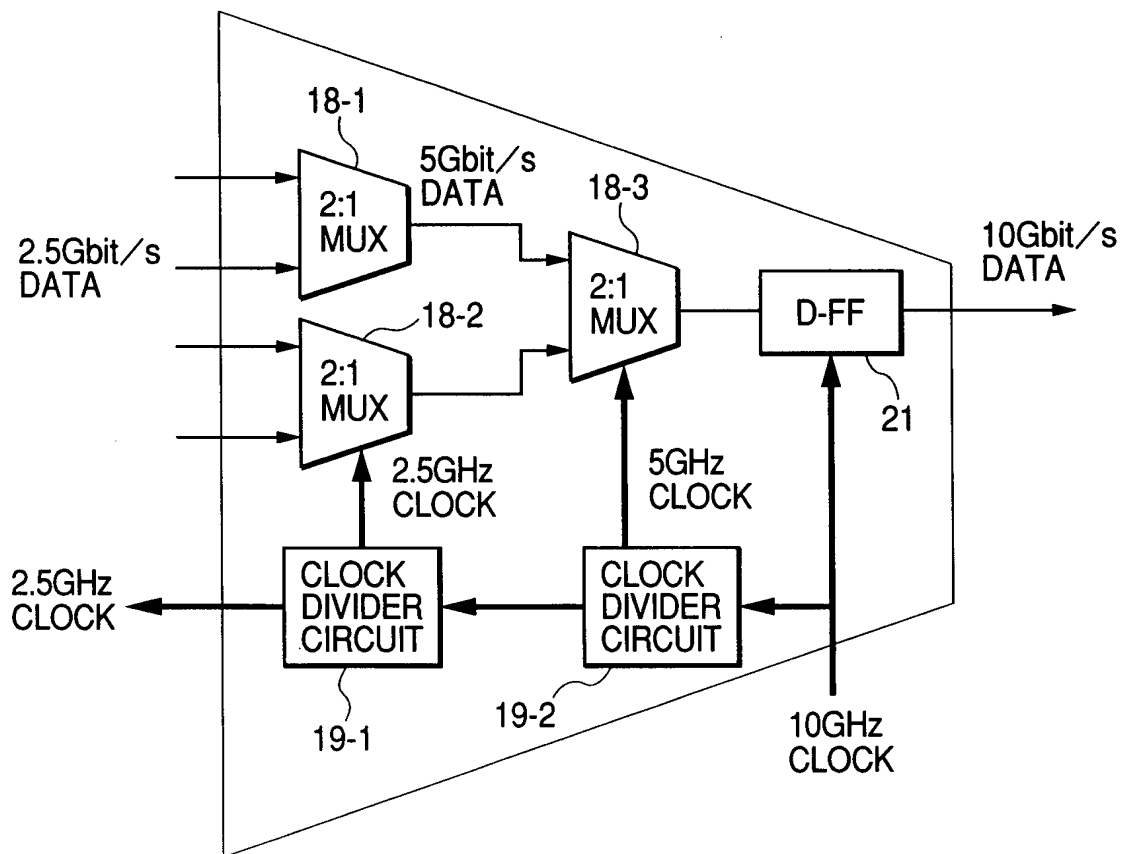
DL: DELAY LINE

PD: PHASE DETECTOR



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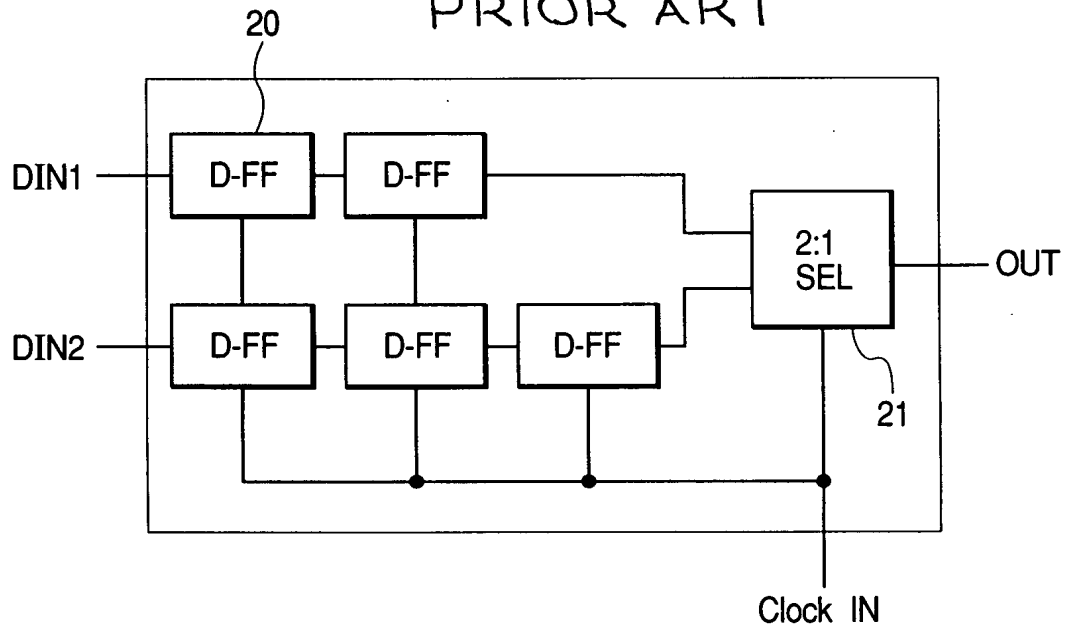
FIG. 4
PRIOR ART





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FIG. 5
PRIOR ART



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FIG. 6
PRIOR ART

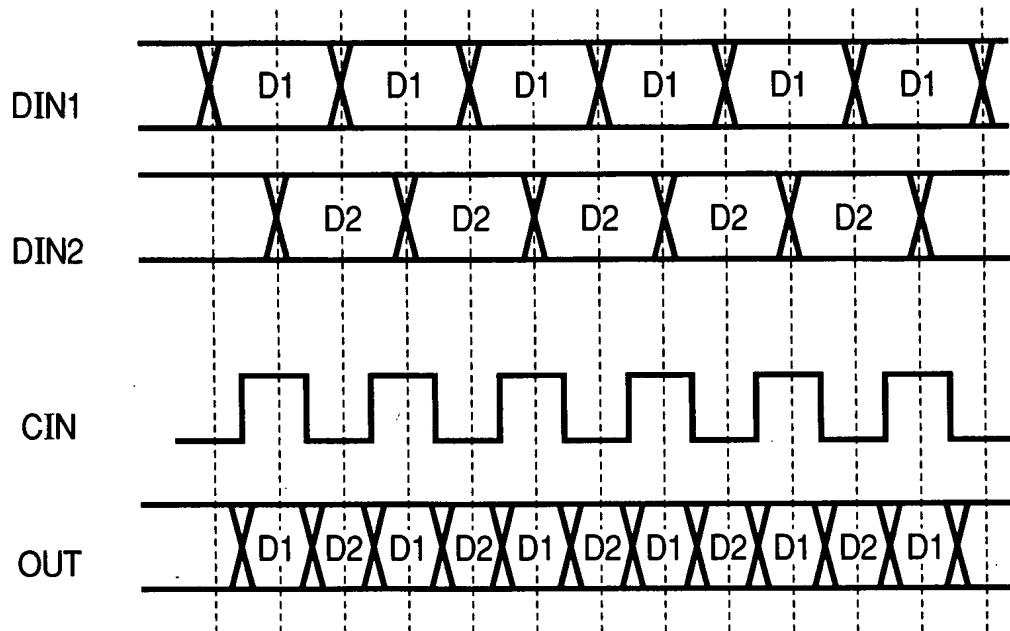


FIG. 15

PRIOR ART

